APPLICANT SUMMARY OF INTERVIEW WITH EXAMINER

In The United States Patent & Trademark Office

Applicant:	Shubhra Venna	Group Art Unit:	2611
Application No.:	10/743,690	Examiner:	Young Tse
Filed:	12/19/2003	Confirmation No.	7790
Title: SYSTEM AND METHOD FOR DIGITAL TRANSMISSION AND			

MODULATION OF CONJUGATE PULSE POSITION

To: Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From: Michael G. Smith

Reg. 45,368

Ivey, Smith and Ramirez

1250 Connecticut Ave NW, #200 Washington, D.C. 20036-2603

(202) 595-1444 x2

REMARKS

In a telephonic interview of Examiner Young Tse on Feb. 18, 2009, Applicant and Examiner agreed that the following amendments place all claims in a state of allowability:

(currently amended) A system comprising:

a pulse width discriminator operable to couple to a data transmission channel, receive a signal from the transmission channel, and detect the signal at a correct sub-slot location,

wherein the signal has $\underline{\text{combined}}$ forward and conjugate pulse positions;

- a clock operably coupled to the pulse width discriminator;
- a demultiplexer operably coupled to the pulse width discriminator and operable to de-multiplex the pulse stream in the signal into corresponding sub-slot positions:
 - a pulse positioner operably coupled to the demultiplexer;
- a conjugate counter operably coupled to the pulse positioner and operbly coupled to the clock;
- a forward counter operably coupled to the pulse positioner and the ${\it clock}$:
- a common slot pulse sorter operably coupled to the conjugate counter and the forward counter;
 - a data conjugator operably coupled to the conjugate counter;
- a data combiner operably coupled to the data conjugator and the forward counter; and
 - a digital-to-analog converter operably coupled to the data combiner,

wherein the system reconstructs an original signal sample from the forward and conjugate pulse positions.

- (previously presented) The system according to claim 1, wherein the forward and conjugate pulse positions are generated by a mono shot pulse generator.
- (currently amended) The system according to claim 1, wherein the
 pulse positioner further comprises: a pulse positioner operably coupled to
 the demuliplexer through three linesthe digital-to-analog converter is
 operably coupled to the clock.
- (previously presented) The system according to claim 1, further comprising:
- a low-pass filter operably coupled to the digital-to-analog converter to generate an analog base band signal from the digital-to-analog converter.
- (currently amended) The system according to claim 1, wherein the system adapts the signal between the forward and conjugate pulse <u>positions</u> in the signal.
- (previously presented)The system according to claim 1, wherein the signal has a thin pulse for forward pulse position coding and a relatively thicker pulse for conjugate pulse position coding.

- (currently amended) The system according to claim 1, wherein the system [[re]] combines the forward and conjugate pulse positions into a digital output.
- 8. (currently amended) A method for transmitting mass quantities of digital data through a data transmission channel at high rates of speed in a communication system-comprising:

splitting input digital data bits/samples into a plurality of data bit/sample sets; and

encoding forward and conjugate pulse positions over the <u>data</u> transmission channel <u>using forward and conjugate time position encoders</u>,

wherein the encoding includes adapting the plurality of data bit/sample sets by separating the plurality of data bit/sample sets into the forward and conjugate pulse positions over the <u>data</u> transmission channel,

wherein a first k-bit representative pulse of the forward pulse position is positioned in a forward manner and a second k-bit representative pulse of the conjugate pulse position is positioned on a conjugate pulse location within a[[the]] same space, and

wherein a thin pulse of the forward pulse position is used for forward pulse position coding and a relatively thicker pulse of the conjugate pulse position is used for conjugate pulse position coding.

 (previously presented)The method according to claim 8, wherein the thin pulse is generated by a forward time position converter and the thicker pulse is generated by a conjugate time position converter. 10. (previously presented)The method according to claim 8, wherein the forward and conjugate pulse positions are generated by a mono-shot pulse generator.

(currently amended)A method comprising:

generating a trailing edge digital pulse width modulated signal from a digital input signal by comparing a sampled signal toagainst a negative slope linear staircase signal having a number of steps, the negative slope staircase signal occupying a[[the]] same intra sample time span of the sampled signal;

generating a leading edge digital pulse width modulated signal from the digital input signal by comparing the sampled signal to a positive slope staircase signal having the same number of steps, wherein a reference staircase is of positive slope having the same number of steps and occupying a[[the]] same intra sample time span[[frame]] of the negative slope staircase signal;

generating a position indicating pulse for each modulated edge of the tailing edge and leading edge of the digital pulse-width modulated signals, yielding a plurality of pulse positions;

multiplexing the <u>plurality of pulse</u> positions into forward and conjugate positioned pulses of different pulse widths;

detecting equivalence between the input signal and <u>the[[a]]</u> negative slope staircase signal followed by a negative edge triggered mono stable, to produce a linear voltage-to-pulse position conversion characteristic; and

generating conjugate[[d]] positioned pulses by generating <u>the</u>[[a]] leading edge digital pulse_[[]] width modulated signal <u>and by generating a</u> subsequentfollowed by a positive edge triggered mono stable that differentiates modulated edges of leading edge digital pulse-width modulated signal[[s]].

[[.]]

- 12. (currently amended)The method according to claim 11, wherein a thin pulse of the forward positioned pulse is used for forward pulse position coding and a relatively thicker pulse of the conjugate positioned pulse is used for conjugate pulse position coding.
- 13. (currently amended) The method according to claim 11, wherein the forward and conjugate positioned pulses are generated by a mono shot pulse generator.
- 14. (currently amended)The method according to claim 11, wherein [[the l]generating the positive slope staircase signals for leading edge digital pulse-width modulated signals further comprises:

charging a capacitor with a constant current source-through a programmable timing generator controlled high frequency switch.

15. (currently amended) The method according to claim 11, wherein the forward and conjugate pulse is co-located and the method further comprise[[s]]:

generating the positive slope staircase signal by charging a capacitor with a constant current source through a programmable timing generator controlled high frequency switcha third pulse width, which is different and larger, compared to the forward and conjugate pulse code widths.

- 16. (previously presented)The method according to claim 11, wherein an encoder is operable to adapt the digital pulse-width modulated signal between the forward and conjugate pulse positions.
- 17. (previously presented) The method according to claim 16, wherein the encoder is operable to use a thin pulse for forward pulse position coding and a relatively thicker pulse for conjugate pulse position coding.
- (currently amended)The method according to claim 11, wherein the forward and conjugate <u>positioned</u> pulses are [[re]]combined into digital output.
- 19. (previously presented) The method according to claim 11 further comprising:

converting an analog input signal into the digital input signal.

20. (previously presented) The method according to claim 19 further comprising:

splitting the digital input signal into a plurality of data bit/sample sets.

Respectfully Submitted,

Dated: Feb 18, 2008 By: Michael G. Smith

45,368 Ivey, Smith and Ramirez 1250 Connecticut Ave NW, #200 Washington, D.C. 20036-2603

(202) 595-1444 x2